

1100	CALL print
1104	
	•
	•
	•
1200	CALL print
1204	
	•
	•
	•
1300	JUMP 1100
1500	sub print
	•
	•
	•
1600	RETURN

FIG. 1

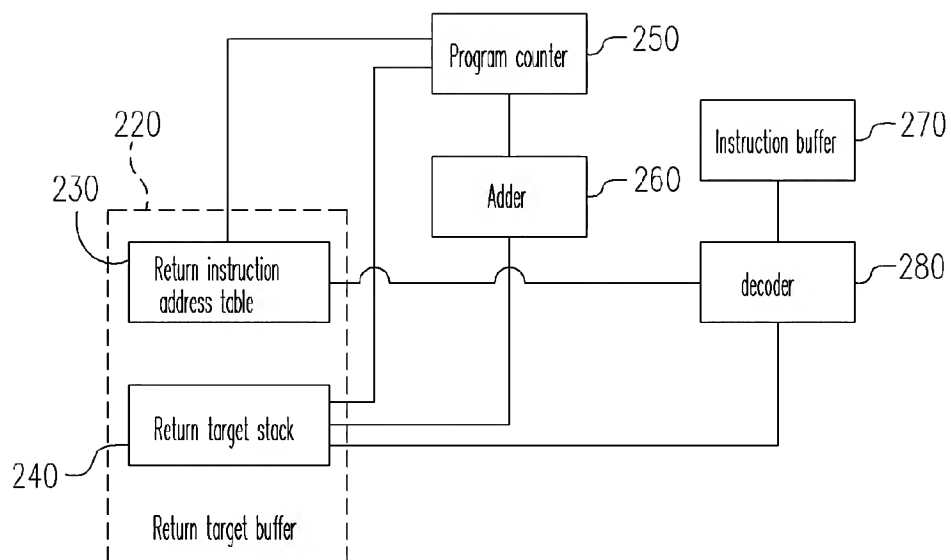


FIG. 2

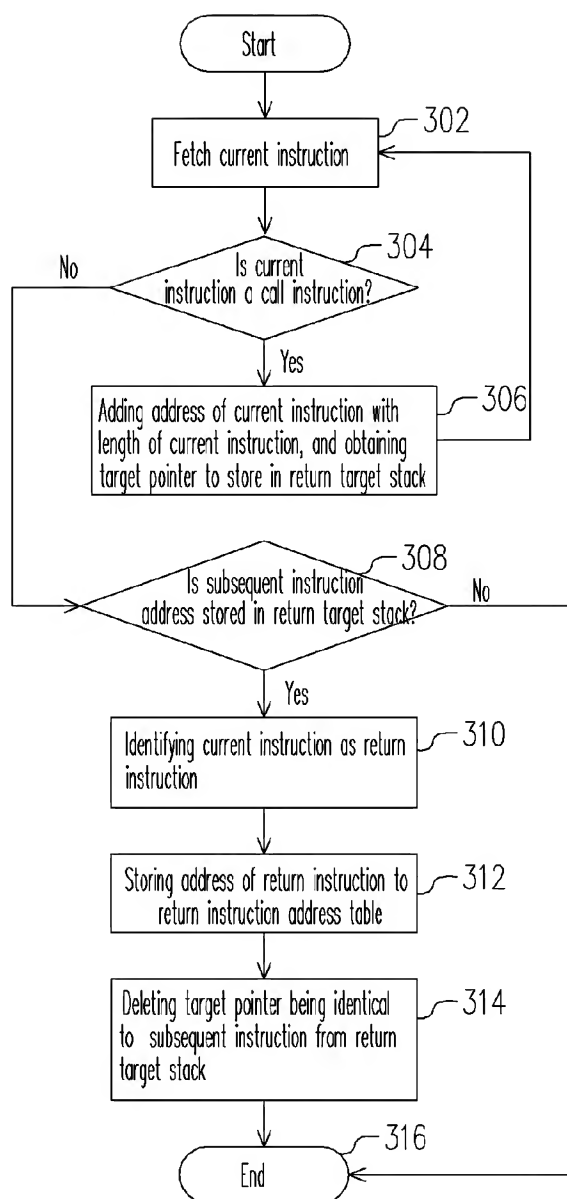


FIG. 3

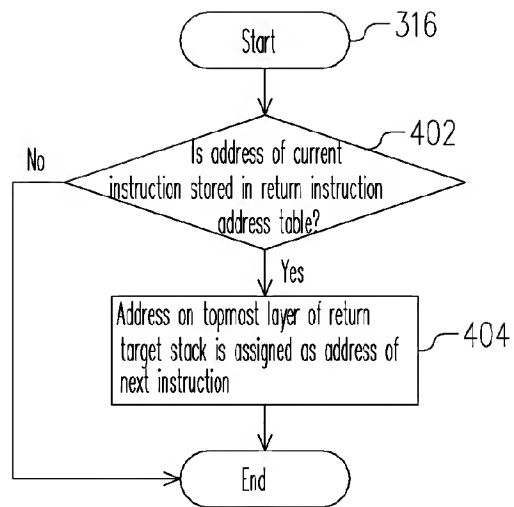


FIG. 4

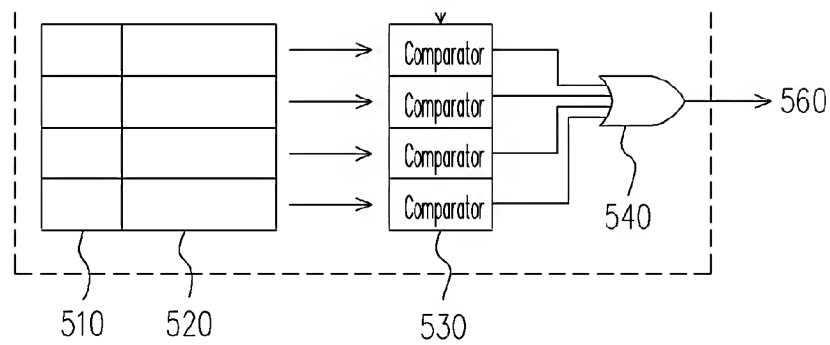


FIG. 5

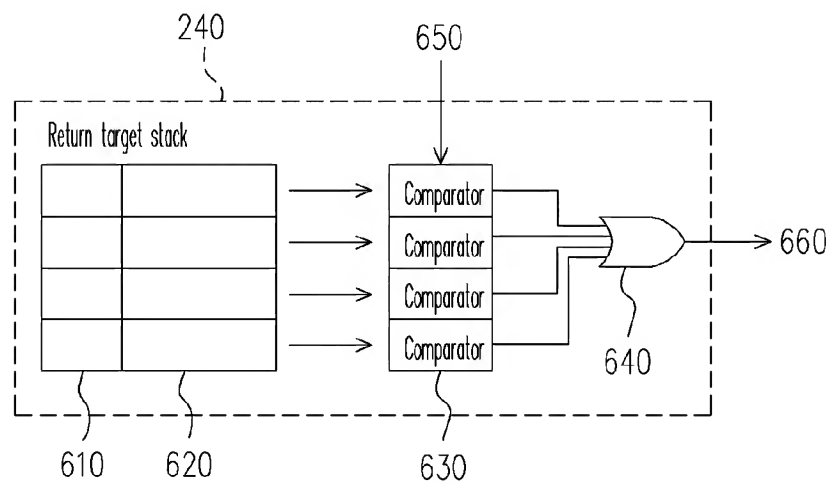


FIG. 6

Return instruction
address table

0	
0	
0	
0	

Return target stack

1	1104
0	
0	
0	

FIG. 7A

Return instruction
address table

1	1600
0	
0	
0	

Return target stack

0	
0	
0	
0	

FIG. 7B

Return instruction
address table

1	1600
0	
0	
0	

Return target stack

1	1204
0	
0	
0	

FIG. 7C

Return instruction
address table

1	1600
0	
0	
0	

Return target stack

0	
0	
0	
0	

FIG. 7D

Return instruction
address table

1	1600
0	
0	
0	

Return target stack

1	1104
0	
0	
0	

FIG. 7E

Return instruction
address table

1	1600
0	
0	
0	

Return target stack

0	
0	
0	
0	

FIG. 7F